Please replace the paragraph beginning at page 7, line 25 with the following rewritten paragraph:

The processor 36 interfaces with a host bus 44 which also interfaces with a bridge circuit 46. The bridge circuit 46 routes data from the host bus 44 to a PCI bus 48. The PCI bus 48 provides a data source to a logic circuit 50 which is provided in the data path between the PCI bus 48 and IDE bus 52 and floppy drive control bus 54 which interface directly with the respective data storage devices 38, 40, 42, 44, and 46. The PCI bus 48 may also receive data from I/O devices 56 via a PCI to ISA bridge circuit 58.

IN THE CLAIMS:

Please cancel Claim 11, without disclaimer or prejudice.

Please amend Claims 1, 5, 7, and 8 as follows:

1.   (Amended) In a personal computer, a method of storing data on one or more magnetic or optical data storage media in an encrypted form comprising:

retrieving an identification code from a non-erasable memory location in said personal computer;

generating a cryptographic key derived at least in part from said identification code;

encrypting data for storage on one of said data storage media using said cryptographic key.

5.   (Amended) A method of making a computer comprising:

storing a hardware identifier in a non-erasable memory integrated circuit;

installing said memory integrated circuit into said computer;

providing a data path to data storage media;

coupling a logic circuit comprising an encryption engine to said data path; and

connecting said memory integrated circuit to said logic circuit.

7.   (Amended) In a computer system comprising host computing logic and at least one data storage device, a method of data storage comprising encrypting user generated data with an encryption process, wherein said encryption process is defined at least in part with